

PATENT

Docket No.: JCLA8533

#4 Election/A  
D. EVANS  
6.19.02

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Applicant : MOU-SHIUNG LIN et al.

Application No. : 10/055,568

Filed : January 22, 2002

For : INTEGRATED CHIP PACKAGE STRUCTURE USING SILICON SUBSTRATE AND  
METHOD OF MANUFACTURING THE SAME

Art Unit : 2827

Examiner : MITCHELL, JAMES M

RESPONSE TO RESTRICTION REQUIREMENT**BOX Non-Fee Amendment**Assistant Commissioner for Patents  
Washington, D.C. 20231

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JUN 17 2002

TECHNOLOGY CENTER 2800

Dear Sir:

In response to the Restriction Requirement mailed on May 21, 2002 regarding the above-identified application, applicant elects Group I, claims 1-60, for further examination on merits. Please cancel claims 61-138 without prejudice and disclaimer.

No fee is believed to be due in connection with the filing of this paper. However, the Commissioner is authorized to charge any additional fees that may be required to Account No. 50-0710 (Order No. JCLA8533).

Respectfully submitted,  
J.C. PATENTSDated: 6/17/2002By: Jiawei Huang  
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**CERTIFICATE OF TRANSMISSION****June 17, 2002**

<b>Atty Docket No.:</b>	JCLA8533
<b>Appl. No.:</b>	10/055,568
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<b>Pages:</b>	Cover + 1

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**Fax No.:** 703-872-9318  
**Attention:** Examiner MITCHELL, JAMES M  
**Group Unit:** 2827  
**From:** Jiawei Huang, Reg. No. 43,330  
**MESSAGE:** Enclosed is a Response to Restriction Requirement in 1 page.

**JUN 17 2002**

TECHNOLOGY CENTER 2800

Sir:

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office on **June 17, 2002** at the above indicated fax number.

Sign by: *Michelle Chang*  
Michelle Chang

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